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DIRECTOR, U.S. PATENT AND TRADEMARK OFFICE WASHINGTON, DC 20231

In re Application of:

COFLER

Serial No.:

10/083,629

Filed:

February 26, 2002

For:

METHOD OF HANDLING INSTRUCTIONS WITHIN A PROCESSOR WITH DECOUPLED ARCHITECTURE, IN PARTICULAR A PROCESSOR FOR DIGITAL SIGNAL PROCESSING, AND CORRESPONDING PROCESSOR

Sir:

Transmitted herewith is an INFORMATION DISCLOSURE STATEMENT in the above-identified application.

- 1. [X] This IDS is submitted under 37 C.F.R. § 1.97. No fee is required.
- 2. [] This IDS is submitted under 37 C.F.R. § 1.97(c). Enclosed is a check in the amount of \$180.00.
- 3. [] This IDS is submitted under 37 C.F.R. § 1.97(c) and (e). No fee is required.
- 4. [] This IDS is submitted under 37 C.F.R. § 1.97(d) and (e). Enclosed is a check in the amount of \$130.00 to cover the petition fee.
- 5. [X] The Commissioner is hereby authorized to charge or credit any discrepancies in fee amounts to Deposit Account No. 01-0484.
- 6. [X] Please associate this application with Customer No. 27975.

PATENT TRADEMARK OFFICE

Date: March 14, 2002

MICHAEL W. TAYLOR

Reg. No. 43,182

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
COFLER

Serial No. 10/083,629

Filing Date: February 26, 2002

For: METHOD OF HANDLING INSTRUCTIONS)
WITHIN A PROCESSOR WITH
DECOUPLED ARCHITECTURE, IN
PARTICULAR A PROCESSOR FOR
DIGITAL SIGNAL PROCESSING, AND

CORRESPONDING PROCESSOR

SET IL STANISHED

CITATION UNDER 37 CFR §1.97

Director, U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

Attached is Form PTO-1449 listing several references for consideration in the examination of the above-identified application. A copy of each reference is also enclosed. It is requested that these references be considered by the Examiner and officially made of record in accordance with the provisions of 37 CFR \$1.97 and Section 609 of the MPEP.

Respectfully submitted,

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Attorney for Applicant

In re Patent Application of:
COFLER

Serial No. 10/083,629

Filing Date: February 26, 2002





CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: DIRECTOR, U.S. PATENT AND TRADEMARK OFFICE, WASHINGTON, D.C. 20231, on this // day of March, 2002.

COPY OF PAPERS ORIGINALLY FILED

INFORMATION DISCLOSURE STATEMENT

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Atty Docket: Serial No.: Applicant: Filing Date: Group:

00GR34554354 10/083,629 COFLER February 26, 2002

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U.S. PATENT DOCUMENTS

U.S. PATENT DOCUMENTS							
Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Dat
	AA	5,664,137	9/2/97	Abramson et al.	395	392	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	Al						
	AJ						
			FOREIGN	PATENT DOCUMEN	TS	<u> </u>	
		Document Number	Date	Country	Class	Sub Class	Translation
	AL	0133477	2/27/85	EP	G06F9	38	
	AM	0840209	5/6/98	EP	G06F9	38	
	AN	1050805	11/8/00	EP	G06F9	38	
	AO						
	,	OTHER ART (i	ncluding Au	ıthor, Title, Date, Pei	tinent Pages	s, etc.)	-
	AP	International	Conference, amitos, IEEE	a Vax for High Perfor Spring Meeting, Los Comp. Soc. Press, U 63	Alamitos, Feb	ruary 26 –	March 2.
	AQ	Yoshida et al., "A Strategy for Avoiding Pipeline Interlock Delays in a Microprocessor" Proceedings of the International Conference on Computer Design: VLSI in Computers and Processors, Cambridge, MA., September 17-19, 1990, pages 14-19, XP000201400					
	AR	Zhang et al., "Performance Modeling and Code Partitioning for the DS Architecture"					

EXAMINER:	DATE CONSIDERED:

vol. 26, no. 3, Specissue, June 1, 1998, pages 293-304, XP000784225

Computer Architecture News, Association for Computing Machinery, New York, US,

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.